

tion registers for the peripherals. Both the DSP core and DMA-capable peripherals can access the DSP's entire memory map.

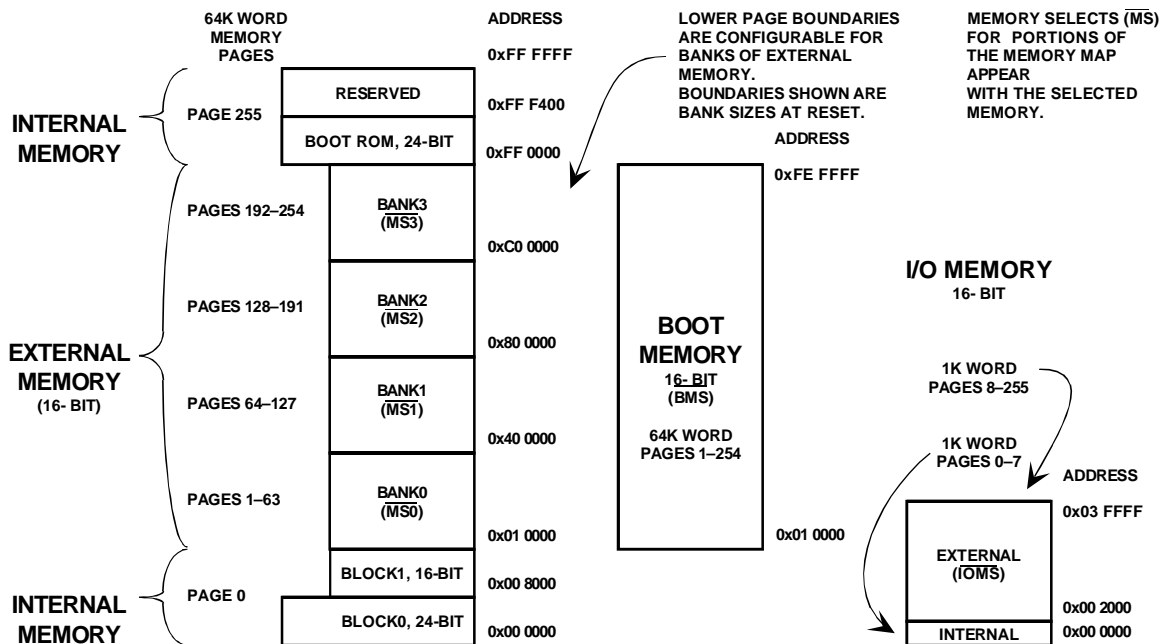


Figure 1-3. ADSP-2191 Internal/External Memory, Boot Memory, and I/O Memory Maps

Internal (On-chip) Memory

The ADSP-2191's unified program and data memory space consists of 16M locations that are accessible through two 24-bit address buses, the PMA and DMA buses. The DSP uses slightly different mechanisms to